



Form PTO-1449 <b>INFORMATION DISCLOSURE CITATION</b> <b>IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	Docket Number (Optional)	Application Number
	2238-080	
	Applicant	
	Kazim Seven, et al.	
	Filing Date	Group Art Unit
	HEREWITH	

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5 6 2 1 3 5 5	4/15/97	Williams et al.	330	107	
	5 9 3 7 0 1 3	8/10/99	Lam et al.	375	340	
	4 1 9 3 6 8 6	3/18/80	Klank	455	154	
	5 2 8 1 9 3 1	1/25/94	Bailey et al.	333	17.1	
	5 5 6 4 0 9 9	10/8/96	Yonekura et al.	455	318	
	5 4 0 4 5 8 9	4/4/95	Bijker et al.	455	200.1	
	5 7 5 8 2 9 6	5/26/98	Nakamura	455	575	
	3 8 9 4 2 8 6	7/8/75	Armstrong	325	438	
	6 1 6 7 2 4 6	12/26/00	Elder et al.	455	313	

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages)*

	2001 IEEE International, Solid-State Circuits Conference, pgs. 198-199, and 202-203.
	Analog Integrated Circuit Design, David A. Johns and Ken Martin, Chapter 15 pgs. 574-575, 578-584, and 626-635.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609-. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.